

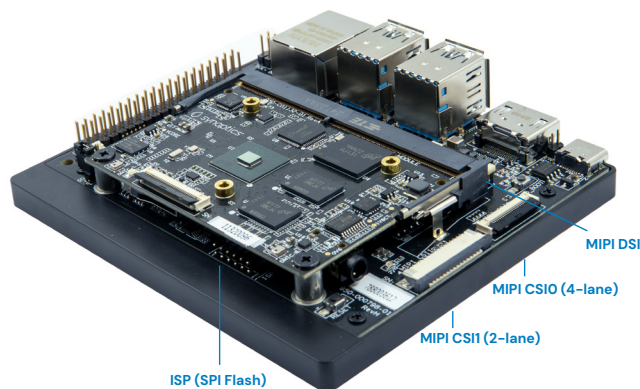


Future-Ready AI-Native Compute for the IoT

MULTIMODAL > SECURE > LOW POWER



The Synaptics Astra™ Machina SL2600 Series development kit enables easy and rapid prototyping of multimodal AI-native IoT applications. A flexible design approach supports a core compute module, an I/O base board, daughter cards for integrated Wi-Fi / Bluetooth connectivity, debug, and programmable I/O. The evaluation system supports the Synaptics SL2619 SoC family that delivers unprecedented levels of price performance for the IoT, and enabled via an open, unified software experience built on Yocto Linux. The Machina SL2600 Series is powered by the open-source Synaptics Torq™ Edge AI platform, leveraging the Coral Open NPU subsystem.



EASY + FAST PROTOTYPING



UNIFIED SOFTWARE EXPERIENCE



COMPREHENSIVE AI TOOLKIT



PRE-PAIRED WIRELESS OPTIONS



MODULAR DESIGN

Features

Core Modules:

- Synaptics SL2610 SoCs
- Storage (eMMC 5.1), memory (DRAM), PMIC, SD card slot
- Audio Input/Output
- DMICs
- RGMII-TX/RX

Daughter Card Interface Options:

- MIPI DSI®, MIPI CSI-2®
- JTAG, 40-pin header
- 4-pin PoE+

I/O Board:

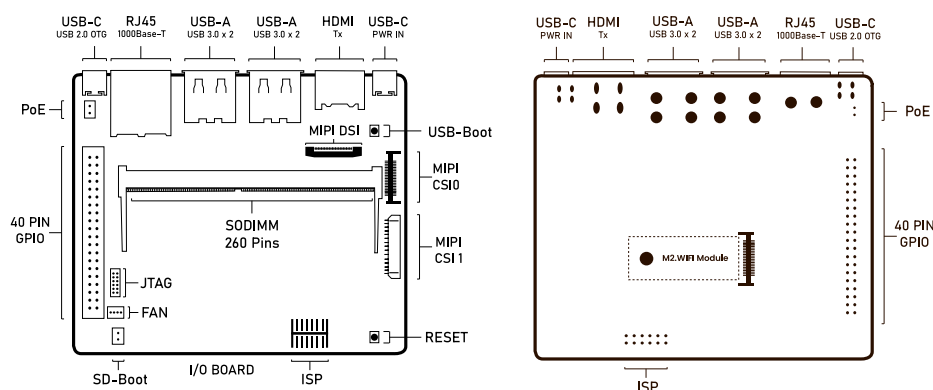
- HDMI Type-A Tx
- M.2 E-key 2230 slot for SDIO, PCIe¹, and UART
- USB 3.0 Type-A: 4 ports, host mode
- USB 2.0 Type-C: OTG host or peripheral mode
- Push buttons: USB-BOOT selection and system RESET
- 2pin Header: SD-BOOT selection
- Type-C power supply with 15V @ 1.8A

¹ SL2600 Series does not support PCIe on the M.2 connector

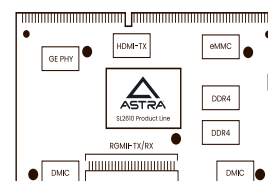
Synaptics Astra Machina

SL2600 Series Dev Kit System Diagrams

I/O Module Front and Back Views



SL2619 Core Module



- SYN4612
- SYN4382
- SYN43711
- SYN43756

* Currently supported, more options to be added.

Resources

- Synaptics Astra GitHub: <https://github.com/synaptics-astra>
- Synaptics Astra AI Developer Zone: <https://developer.synaptics.com/>



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